



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Thomas A. Tetzlaff

Title: DIGITAL CIRCUIT SIMULATION

Docket No.: 884.279US1

Filed: June 30, 2000

Examiner: Samuel Broda

Customer No.: 21186

Serial No.: 09/607,421

Due Date: August 15, 2005

Group Art Unit: 2123

Confirmation No.: 9595

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

**Notice of Allowance Date:**

May 13, 2005

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☒ Comment on the Examiner's Statement of Reasons for Allowance (1 pg.).

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number: 21186

By \_\_\_\_\_

Robert E. Mates

Reg. No. 35,271

REM:CMG:jdgw

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John D. Gistler, Wrathell  
Name

John D. Gistler, Wrathell  
Signature



S/N 09/607,421

PATENT

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Title: DIGITAL CIRCUIT SIMULATION

Assignee: Intel Corporation

Customer Number: 21186

**COMMENT ON THE EXAMINER'S STATEMENT  
OF REASONS FOR ALLOWANCE**

MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

An Examiner's Statement of Reasons for Allowance is attached to the Notice of Allowability dated 13 May 2005. The Statement paraphrases text from some of the allowed claims 1-6, 16-20, 26-28, and 29-34. The applicant respectfully submits that the phrasing recited in the Examiner's Statement does not appear word-for-word in all of the listed claims. Nor does the Examiner's Statement include all of the features recited. The combination of features claimed in each of the allowed claims 1-6, 16-20, 26-28, and 29-34 is unique. None of the allowed claims are shown or suggested by the prior art.

The Examiner is invited to telephone Applicant's attorney at (612) 373-6973 to facilitate the prosecution of this application. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,  
THOMAS A. TETZLAFF  
By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date 12 AUGUST 2005

By

Robert E Mates  
Reg. No. 35,271

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Name

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